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Project Two Report

In the second project, we were asked to utilize a project from CECS 201, which demonstrated how to construct a counter in Verilog using a register as fundamental building block. In this project, I added the circuitry to produce the seven-segment decoder, which took the value present in the 32-bit counter and displayed it on the eight seven segment LEDs. This project was cool because I got to see my project on the board through the LED display and understood what was happening. The only programmable button on the board was the reset, which reset the count. The project also helped me understand how LED takes in input and converts them to specific alphabetic values. Even though in this project we only used 1-9 and A-F, this idea can be used to display any sort of character on the board.